


MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

List of Students Registered for Value Added Course

2015-16

S.No	Branch	Roll Number	Student Name	Year/ Sem	Value Added Course	DATE OF INTRODUCTI ON	DATE OF COMPLETION
1	ECE	13J41A0403	BOYINI RAJASHEKAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
2	ECE	13J41A0416	GOLLA ANURADHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
3	ECE	13J41A0419	JAJULA SAIRAM	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
4	ECE	13J41A0421	K MAHENDRA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
5	ECE	13J41A0433	MOHAMMED AMIR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
6	ECE	13J41A0436	SRIKANTH.ODETI	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
7	ECE	13J41A0437	PADIGELA SAIKIRAN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
8	ECE	13J41A0440	R SAI PRIYANKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
9	ECE	13J41A0446	SHAIK.SHARUF	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
10	ECE	13J41A0449	THOUTIREDDYSRINIJA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
11	ECE	13J41A0452	U.PRAMODH KUMAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016


PRINCIPAL
Malla Reddy Engineering College
(Autonomous)
Maisammaguda, Dhulapally,
(Post Via Kompally), Sec'bad-500 1



12	ECE	13J41A0454	V BHARANI REDDY	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
13	ECE	13J41A0455	V.L.NKITHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
14	ECE	13J41A0457	VEERAGONI VENU	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
15	ECE	13J41A0459	Y GANGADHAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
16	ECE	13J41A0460	KONDRA RAJU	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
17	ECE	14J45A0411	JADALA THARUN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
18	ECE	13J41A0461	ADI LENIN JAIDEEP	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
19	ECE	13J41A0463	A JOHN VINEETH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
20	ECE	13J41A0465	B.DAMODHAR	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
21	ECE	13J41A0472	G CHANDRAKANTH	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
22	ECE	13J41A0474	GAJULAVAMSHI	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
23	ECE	13J41A0475	G.SAI TARUN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
24	ECE	13J41A0479	K JUNIPER SARDONYX	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
25	ECE	13J41A0480	K MOUNIKA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
26	ECE	13J41A0482	KADUDULA SAIKIRAN	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016
27	ECE	13J41A0485	KOMMUKURI.SABITHA	III/II	Mixed Signal Design & Verification Using Cadence Tools	24-02-2016	28-02-2016

